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| VIFISP Case | Status | comment(mipi) |
| Pattgen+ one\_path+(RGB, RAW) | PASS |  |
| Pattgen+ two\_path+(RGB, RAW) | PASS |  |
| raw\_data+ one\_path | PASS |  |
| yuv\_data+ one\_path | <PASS> | YUV420\_8 |
| rgb\_data+ one\_path | <PASS> | Change model (RGB888) |
| raw\_data+ two\_path | <PASS> |  |
| yuv\_data+ twoth | <PASS> | YUV420\_8 |
| rgb\_data+ twoth | < PASS> | Change model (RGB888) |
| Interrupt🡪 fifo\_full | < PASS> | Patgen check. Htotal: length\*2🡪length\*4 |
| Interrupt🡪single\_frame\_mode | <PASS> |  |
| Interrupt🡪config change | <PASS> | Single frame 🡪sequence mode, initial time 1🡪2 |
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| DVP601(1 path)+ISPIF | <PASS> | 1920x1080 YUV422\_8 |
| DVP601(2 path)+ISvis PIF | <PASS> | 1920x1080 YUV422\_8 |
| DVP656(1 path)+ISPIF | PASS | 1920x1080 YUV422\_8 |
| DVP656(2 path)+ISPIF | PASS | 1920x1080 YUV422\_8 |
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| VIFBP Case | Status | Comment(mipi) |
| one\_path+mipi+raw | Pass | Raw14, 1080P |
| two\_path+mipi+raw | pass | Raw14, 1080P |
| 4-path+mipi+raw | pass | Raw14, 1080P |
| 8-path+mipi+raw | pass | Raw14, 1080P |
| one\_path+mipi+yuv | pass | Yuv420\_8, 1080P |
| two\_path+mipi+yuv | pass | Yuv420\_8, 1080P |
| 8-path+mipi+yuv | pass | Yuv422\_8, 1080P |
| 1\_path+mipi+rgb | Pass | Rgb888, 1080P |
| 8-path+mipi+rgb | pass | Rgb888, 720p |
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| one\_path+hdmi | ok | 1080p, rgb888 |
| Hdmi+audio |  |  |
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| 1path+dvp601+yuv422 | ok | 1920x1080p60, sdr 16bit |
| 2path+dvp601+yuv422 | ok | 720p60, ddr 8bit |
| 1path+dvp656+yuv422 | ok | sdr 16bit |
| 2path+dvp656+yuv422 | ok | ddr 8bit |
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| MIPIRX Case | Status | Comment(mipi) |
| x8 720P\*30(60) RAW DDR | Multi-channel data: DDR first, then to ISP. (nihao2) | Ok |
| x8 720P\*(60) YUV420 DDR | 8-bit (tbx) | Ok |
| x8 720P\*(60) YUV420 DDR | 10-bit (nihao) | Ok |
| x8 720P\*(60) YUV422 DDR | 8-bit | pass |
| x8 1080P\*30 RAW to DDR | Max support, key feature, raw14 | Y <PASS> |
| x2 1080P\*30 RAW to ISP | Raw14 | <PASS> |
| 2in1 >4k RAW DDR | Isp测过了 | 无需重复测试 |
| 4k\*30fps\*14bit or 4k\*60fps\*8bit RAW to ISP directly | key feature  isp测过了 | Y  无需重复测试 |
| CCI - Master |  |  |
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| DVPRX Case | Status | Comment(mipi) |
| 1080P\*60 YUV422 to DDR 16bit | ok | Dvp601/dvp656, sdr 16bit |
| 720P\*60 YUV422 to DDR 8bit | ok | Dvp601, ddr 8bit |
| 1in & 1out 720P\*60 YUV422 8 bit |  |  |
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| DVPTX Case | Status | Comment(mipi) |
| 1in & 1out 720P\*60 YUV422 8 bit |  |  |
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| MIPITX Case | Status | Comment(mipi) |
| 4k\*30 YUV422/420 8bit |  |  |
| 1080P\*60 RGB 8bit |  |  |
| CCI - Slave |  |  |
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